Systematic Development of Correct Bulk Synchronous Parallel Programs

Louis Gesbert*, Zhenjiang Hu†, Frédéric Loulergue‡, Kiminori Matsuzaki§ and Julien Tesson¶
*MLstate, Paris, France, louis.gesbert@mlstate.com
†National Institute of Informatics, Tokyo, Japan, hu@nii.ac.jp
‡LIFO, Université d'Orléans, France, Frederic.Loulergue@univ-orleans.fr
§Kochi University of Technology, Tokyo, Japan, matsuzaki.kiminori@kochi-tech.ac.jp
¶LIFO, Université d'Orléans, France, Julien.Tesson@univ-orleans.fr

***Abstract*— With the current generalisation of parallel architectures arises the concern of applying formal methods to parallelism. The complexity of parallel, compared to sequential, programs makes them more error-prone and difficult to verify. Bulk Synchronous Parallelism (BSP) is a model of computation which offers a high degree of abstraction like PRAM models but yet a realistic cost model based on a structured parallelism. We propose a framework for refining a sequential specification toward a functional BSP program, the whole process being done with the help of the Coq proof assistant. To do so we define BH, a new homomorphic skeleton, which captures the essence of BSP computation in an algorithmic level, and also serves as a bridge in mapping from high level specification to low level BSP parallel programs.**

I. Introduction

With the current generalisation of parallel architectures and increasing requirement of parallel computation arises the concern of applying formal methods, which allow specifications of parallel and distributed programs to be precisely stated and the conformance of an implementation to be verified using mathematical techniques. However, the complexity of parallel programs, compared to sequential ones, makes them more error-prone and difficult to verify. This calls for a strongly structured form of parallelism [18], [22], which should not only be equipped with an abstraction or model that conceals much of the complexity of parallel computation, but also provide a systematic way of developing such parallelism from specifications for practically nontrivial examples.

The Bulk Synchronous Parallel (BSP) model is a model for general-purpose, architecture-independent parallel programming [10], [28]. The BSP model consists of three components, namely a set of processors each with a local memory, a communication network, and a mechanism for globally synchronising the processors. A BSP program proceeds as a series of super-steps. In each super-step, a processor may operate only on values stored in local memory. Values sent through the communication network are guaranteed to arrive at the end of a super-step. Although the BSP model is simple and concise, it remains as a challenge to systematically develop efficient and correct BSP programs that meet given specifications.

To see this clear, consider the following tower-building problem, which is an extension of the known line-of-sight problem [4]. Given a list of locations (position x_i and height h_i) along a line in the mountain (see Fig. 1):

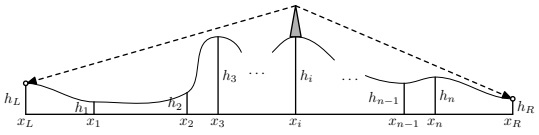

Fig. 1
TOWER-BUILDING PROBLEM

$$[(x_1, h_1), \ldots, (x_i, h_i), \ldots, (x_n, h_n)]$$

and two special points (x_L, h_L) and (x_R, h_R) on the left and right of these locations along the same line, the problem is to find all locations from which one can see the two points after building a tower of height h. If we do not think about efficiency and parallelism, this problem can be easily solved by considering for each location (x_i, h_i), whether it can be seen from both (x_L, h_L) and (x_R, h_R). The tower with height h at location (x_i, h_i) can be seen from (x_L, h_L) if for any $k = 1, 2, \ldots, i-1$ the inequality

$$\frac{h_k - h_L}{x_k - x_L} < \frac{h + h_i - h_L}{x_i - x_L}$$

holds. Similarly, it can be seen from (x_R, h_R) means that for any $k = i+1, \ldots, n$, the inequality

$$\frac{h_k - h_R}{x_R - x_k} < \frac{h + h_i - h_R}{x_R - x_i}$$

holds. While the specification is clear, its BSP parallel program, say in BSML [8], a library for BSP programming in the functional language Objective Caml, is rather complicated. This gap makes it difficult to verify that the implementation is correct with respect to the specification.

In this paper, we propose the first general framework (in Sect. II), as far as we are aware, for systematic development of certified functional BSP parallel programs. More specifically, (1) we introduce a novel algorithmic skeleton (Sect. III), BSP Homomorphism (or *BH* for short), which can not only capture the essence of BSP computations at algorithmic level, but also serve as a bridge by mapping high level specification to low level BSP parallel programs; (2) we develop a set of useful

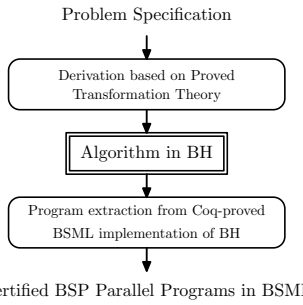

Fig. 2
AN OVERVIEW OF OUR FRAMEWORK FOR DEVELOPING CORRECT BSP PROGRAMS

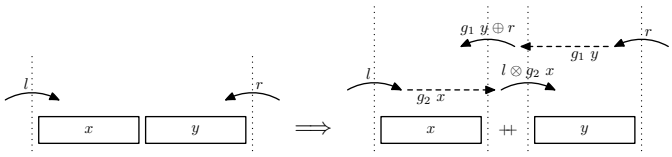

Fig. 3
INFORMATION PROPAGATION FOR *BH*.

theories (Sect. IV) in Coq for systematic and formal derivation of programs from specification to *BH*, and we provide a certified parallel implementation (Sect. V) of *BH* in BSML so that a certified BSP parallel program can be automatically extracted; and (3) we demonstrate with examples that our new framework can be very useful to develop certified BSP parallel programs for solving various nontrivial problems (Sect. VI).

II. AN OVERVIEW

Figure 2 depicts an overview of our framework. We insert a new layer, called "algorithm in *BH*", between problem specifications and certified BSP parallel programs, so as to divide the development process into two easily tacking steps: A formal derivation of algorithms from specification to *BH* and a proof of correctness of a BSML implementation of *BH*.

In our framework, a specification is described in Coq [26], allowing the user to be confident in its correctness without concern of parallelism. We chose to take Coq definitions as specifications for reasons of simplicity of our system, and for giving access to the full strength of the Coq assistant to prove initial properties of the algorithms (the system will then provide a proof that these properties are preserved throughout the transformations). In the first step, we rewrite the specification into a program using the *BH* skeleton, in a semi-automated way. To do so, we provide a set of Coq theories over *BH* and tools to make this transformation easier. This transformation is implemented in Coq, and proved to be correct, *i.e.* preserving the semantics of the initial specification. Thus, this step converts the original specification into a program (using BH) that is proved equivalent. In the second step, we replace the calls to the skeleton *BH* in the algorithm with a parallel implementation (in BSML) that is proved correct. By using the program extraction features of Coq on the rewritten algorithm, we get a parallel program that implements the algorithm of the specification, and that is proved correct.

III. A BSP HOMOMORPHISM

Homomorphisms play an important role in both formal derivation of parallel programs [7], [12], [14], [16] and automatic parallelisation [20]. Function h is said to be a *homomorphism*, if it is defined recursively in the form of

$$\begin{cases} h\ [\,] & = & id_{\odot} \\ h\ [a] & = & f\ a \\ h\ (x +\!\!+ y) & = & (h\ x) \odot (h\ y) \end{cases}$$

where applying a function f to an expression x is written $f\ x$, $[x_1, \ldots, x_n]$ denotes the list containing the elements x_1 to x_n, $+\!\!+$ denotes list concatenation, $id_{\odot}$ denotes the identity unit of $\odot$. Since h is uniquely determined by f and $\odot$, we will write $h = (\!|\odot, f|\!)$.

Though being general, different parallel computation models would require different specific homomorphisms together with a set of specific derivation theories. For instance, the distributed homomorphism [11] is introduced to treat the hyper-cube style of parallel computation, and the accumulative homomorphism [15] is introduced to treat the skeleton parallel computation.

Our BH (BSP Homomorphism) is a specific homomorphism carefully designed for systematic development of certified BSP algorithms. The key point is that we formalise "data waiting" and "synchronisation" in the super-step of the BSP model by computation of gathering necessary information around for each element of a list and then perform computation independently to update each element.

Definition 1 (BSP Homomorphism): Given function k, two homomorphisms g_1 and g_2, and two associative operators $\oplus$ and $\otimes$, a function bh is said to be a BSP Homomorphism or *BH*, if it is defined in the following way.

$$\begin{cases} bh\ [a]\ l\ r & = & [k\ a\ l\ r] \\ bh\ (x +\!\!+ y)\ l\ r & = & bh\ x\ l\ (g_1\ y \oplus r)\ +\!\!+ \\ & & bh\ y\ (l \otimes g_2\ x)\ r \end{cases}$$

The above bh defined with functions k, g_1, g_2, and associative operators $\oplus$ and $\otimes$ is denoted as

$$bh = BH(k, (g_1, \oplus), (g_2, \otimes)).$$

Function bh is a *higher-order* homomorphism, which takes a list as input and returns a new list of the same length. In addition to the input list, bh has two additional parameters, l and r, which contain necessary information to perform computation on the list. The information of l and r, as defined in the second equation and shown in Fig. 3, is propagated from left and right with functions $(g_2, \otimes)$ and $(g_1, \oplus)$ respectively.

It is worth remarking that BH is powerful; it cannot only describe super-steps of BSP computation, but is also powerful enough to describe various computation including all homo-

morphisms (map and reduce) (Sect. IV), scans, as well as the BSP algorithms in [10].

IV. Deriving Algorithms in BH

In this section, we show how to derive correct algorithms in terms of BH from problem specifications. The specification gives a direct solution to the problem, where one does not need to think about low level parallel computation issues, such as layout of processors, task distribution, data communication. This specification will be transformed into an equivalent algorithm in terms of BH based on a set of transformation theorems.

A. Specification

Coq functions are used to write specification, from which an algorithm in BH is to be derived. Recursions and the well-known collective operators (such as map, fold, and scan) can be used in writing specification. To ease description of computation using data around, we introduce a new collective operator $mapAround$.

The $mapAround$, compared to map, describes more interesting independent computation on each element of lists. Intuitively, $mapAround$ is to map a function to each element (of a list) but is allowed to use information of the sublists in the left and right of the element, e.g.,

$$\begin{array}{l} mapAround\ f\ [x_1, x_2, \ldots, x_n] = \\ \quad [\ f\ ([], x_1, [x_2, \ldots, x_n]), f\ ([x_1], x_2, [x_3, \ldots, x_n]), \\ \quad\quad \ldots, f\ ([x_1, x_2, \ldots, x_{n-1}], x_n, [])\ ]. \end{array}$$

In addition, we provide a set of communication functions such as $permute$, $shiftL$, $shiftR$ to redistribute list elements. They are designed to be not only useful for reconstructing lists in the specification level but also equipped with low lever certified BSP implementations.

Example 1 (Specification of the Tower-Building Problem): Recall the tower-building problem in the introduction. We can solve it directly using $mapAround$, by computing independently on each location and using informations around to decide whether a tower should be built at this location. So our specification can be defined straightforwardly as follows.

$$\begin{array}{l} tower\ (x_L, h_L)\ (x_R, h_R)\ xs = mapAround\ visibleLR\ xs \\ \quad \textbf{where}\ visibleLR\ (ls, (x_i, h_i), rs) = \\ \qquad\qquad visibleL\ ls\ x_i \wedge visibleR\ rs\ x_i \\ \qquad visibleL\ ls\ x_i = maxAngleL\ ls < \frac{h+h_i-h_L}{x-x_L} \\ \qquad visibleR\ rs\ x_i = maxAngleR\ rs < \frac{h+h_i-h_R}{x_R-x} \end{array}$$

The inner function $maxAngleL$ is to decide whether the left tower can be seen, and is defined as follows (where $a \uparrow b$ returns the bigger of a and b).

$$\begin{array}{lcl} maxAngleL\ [\,] & = & -\infty \\ maxAngleL\ ([(x,h)] +\!\!+ xs) & = & \frac{h-h_L}{x-x_L} \uparrow maxAngleL\ xs \end{array}$$

and the function $maxAngleR$ can be similarly defined. □

Example 2 (Maximum Prefix Sum Problem Specification): Consider the maximum prefix sum problem [4], which is to compute the maximum sum of all the prefix sublists. For instance, supposing mps is the function that solves the problem, we have

$$mps\ [1, -1, 2] = 1 \uparrow (1 + (-1)) \uparrow (1 + (-1) + 2) = 2$$

We have two ways to solve this problem. One is to decompose this problem to smaller ones, each of which can be described with our list functions, and then compose them together. Following this idea, we could solve the maximum prefix sum problem by first enumerating all the prefix sums and then compute their maximum.

$$\begin{array}{l} mps = maximum \circ psums \\ \quad \textbf{where}\ maximum = (\!|\uparrow, id|\!) \\ \qquad\quad psums = scan\ (+) \end{array}$$

Certainly, there could be other specification for the same problem. For example, we could solve this problem as by the following recursive function (both leftwards and rightwards).

$$\left\{ \begin{array}{lcl} mps\ [\,] & = & 0 \\ mps\ ([a] +\!\!+ x) & = & 0 \uparrow (a + mps(y)) \\ mps\ (x +\!\!+ [a]) & = & mps(x) \uparrow (sum(x) + b) \end{array} \right.$$

Here sum can be defined similarly and its definition is omitted here. □

B. Theorems for Deriving BH

Since our specification is a simple combination of collective functions and recursive functions, derivation of a certified BSP parallel program can be reduced to derivation of certified BSP parallel programs for all these functions, because the simple combination is easy to be implemented by composing supersteps in BSP.

While simple communication functions may be mapped directly to certified BSP parallel programs, it is more difficult for the collective functions and recursive functions, which may be parametrised with other functions and have more flexible computation structure. Our idea is to map these functions into *BH*, and then show how *BH* can be mapped to a certified BSP parallel programs.

First, let us see how to deal with collective functions. The central theorem for this purpose is the following theorem.

Theorem 1 (Parallelisation $mapAround$ with BH): For a function

$$h = mapAround\ f$$

if we can decompose f as $f\ (ls, x, rs) = k\ (g_1\ ls, x, g_2\ rs)$, where k is any function and g_i is a composition of a function p_i with a homomorphism $h_i = (\!|\oplus_i, k_i|\!)$, then

$$h\ xs = BH(k', (h_2, \oplus_2), (h_1, \oplus_1))\ xs\ \iota_{\oplus_1}\ \iota_{\oplus_2}$$

$$\text{where} \left\{ \begin{array}{l} k'\ (l, x, r) = k(p_1\ l, x, p_2\ r) \text{ holds}, \\ \iota_{\oplus_1} \text{ is the (left) unit of } \oplus_1, \\ \iota_{\oplus_2} \text{ is the (right) unit of } \oplus_2\,. \end{array} \right.$$

Proof: This has been proved by induction on the input list of h with Coq (available in [27]). ■

Theorem 1 is general and powerful in the sense that it can parallelise not only $mapAround$ but also other collective functions to *BH*.

For instance, the useful *scan* computation

$$scan\ (\oplus)\ [x_1, x_2, \ldots, x_n] = [x_1, x_1 \oplus x_2, \ldots, x_1 \oplus x_2 \oplus \cdots \oplus x_n]$$

is a special $mapAround$: $scan\ (\oplus) = mapAround\ f$ where $f(ls, x, rs) = first\ ((\![\oplus, id]\!)\ ls, x, [])$ and *first* returns the first component of a triple.

What is more important is that any homomorphism can be parallelised with *BH*, which allows us to utilise all the theories [7], [12], [14], [16], [20] that have been developed for derivation of homomorphism.

Corollary 1 (Parallelisation Homomorphism with BH*):* Any homomorphism $(\![\oplus, k]\!)$ can be implemented with a *BH*. *Proof:* Notice that $(\![\oplus, k]\!) = last \circ mapAround\ f$ where $f(ls, x, rs) = ((\![\oplus, k]\!) xs) \oplus (k\, x)$. It follows from Theorem 1 that the homomorphism can be parallelised by a *BH*. ■

Now we consider how to deal with recursive functions. This can be done in two steps. We first use the existing theorems [12], [14], [20] to obtain homomorphisms from recursive definitions, and then use Corollary 1 to get *BH* for the derived homomorphisms.

It is worth noting that homomorphisms are very important in all our derivations of *BH*, not only because *BH* itself is a specific homomorphism, but also because many of our derivations go along with derivations of homomorphisms. For example, in Theorem 1, our main theorem, we have to derive homomorphisms so that function f can be defined in a way that the theorem can be applied.

Example 3 (Derivation for the Tower-Building Problem): From the specification given before, we can see that Theorem 1 is applicable with

$$\begin{array}{l} visibleLR\ (ls, x, rs) = k\ (g_1\ ls, x, g_2\ rs) \\ \quad \textbf{where}\ g_1 = maxAngleL \\ \qquad\quad g_2 = maxAngleR \\ \qquad\quad k\ (maxl, (x_i, h_i), maxr) = \\ \qquad\qquad (maxl < \frac{h+h_i-h_L}{x-x_L}) \wedge (maxr < \frac{h+h_i-h_R}{x_R-x}) \end{array}$$

provided that g_1 and g_2 can be defined in terms of homomorphisms.

By applying the theorems in [12], [14], [20], we can easily obtain the following two homomorphisms (the detailed derivation is beyond the scope of this paper).

$$\begin{array}{l} maxAngleL = (\![\uparrow, k_1]\!) \quad \textbf{where}\ \uparrow = max;\ k_1\ (x, h) = \frac{h-h_L}{x-x_L} \\ maxAngleR = (\![\uparrow, k_2]\!) \quad \textbf{where}\ k_2\ (x, h) = \frac{h_R-h}{x_R-x} \end{array}$$

Therefore, applying Theorem 1 yields the following result in *BH*.

$$\begin{array}{l} tower\ (x_L, h_L)\ (x_R, h_R)\ xs = \\ \quad BH(k, (maxAngleL, \uparrow), (maxAngleR, \uparrow))\ xs\ (-\infty)\ (-\infty) \\ \quad \textbf{where}\ k\ (maxl, (x_i, h_i), maxr) \\ \qquad\quad = (maxl < \frac{h+h_i-h_L}{x-x_L}) \wedge (maxr < \frac{h+h_i-h_R}{x_R-x}) \end{array}$$ □

Example 4 (Maximum Prefix Sum Problem Derivation): Derivation of *BH* from the specification

$$\begin{array}{l} mps = maximum \circ psums \\ \quad \textbf{where}\ maximum = (\![\uparrow, id]\!) \\ \qquad\quad psums = scan\ (+) \end{array}$$

given above is straightforward, because it is a composition of a homomorphism and a scan, both of which can be mapped to *BH* according to Theorem 1 and Corollary 1. □

C. Theorem Implementation in Coq

The Coq proof assistant [2], [3], [26] is based on the calculus on inductive construction. This calculus is a higher-order typed λ-calculus. Theorems are types and their proofs are terms of the calculus. The Coq systems helps the user to build the proof terms and offers a language of tactics to do so. Coq is also a functional programming language. In appendix we give a very short introduction to Coq.

We use the Coq proof assistant to prove correct the derivations from a functional specification to a BH skeleton instantiation. Derivations and parallel term retrieving is automated using a newly introduced feature in Coq: Type classes [24].

The full code can be downloaded [27].

V. BH to BSML: Certified Parallelism

We have until now supposed a certified parallel implementation of BH on which the algorithms rely. This implementation is realized using Bulk Synchronous Parallel ML (BSML) [8], an efficient BSP [10], [28] parallel language based on Objective Caml and with formal bindings and definitions in Coq. In Coq, we prove the equivalence of the natural specification of BH with its implementation in BSML, therefore being able to translate the previous BH certified versions of the algorithms to a parallel, BSML version. We also analyse the parallel performances of this implementation.

A. Bulk Synchronous Parallel ML: An Overview

a) Bulk synchronous parallelism: A BSP machine can be though as a homogeneous distributed memory machine with a unit able to synchronise all the processors. A BSP program is executed as a sequence of *super-steps*, each one divided into (at most) three successive and logically disjointed phases: (a) Each processor uses its local data (only) to perform sequential computations and to request data transfers to/from other nodes; (b) the network delivers the requested data transfers; (c) a global synchronisation barrier occurs, making the transferred data available for the next super-step.

The performance of a BSP machine is characterised by 3 parameters: p is the number of processor-memory pairs, L is the time required for a global synchronisation and g is the time for collectively delivering a 1-relation (communication phase where every processor receives/sends at most one word). The network can deliver an h-relation in time $g \times h$ for any arity h. The BSP parameters can be determined in practice using benchmarks: first a fourth parameters r, the computing power of the processors is determined (in flop/s), then some g' and L'

are measured in $s/word$ and s, and the final BSP parameters are $g = g' \times r$ (in flop/word) and $L = L' \times r$ (in flop).

The execution time (or *cost*) of a super-step is thus the sum of the maximal local processing time, of the data delivery time and of the global synchronisation time:

$$\max_{0 \leq i < \text{bsp\_p}} w_i + \max_{0 \leq i < \text{bsp\_p}} \max(h_i^+, h_i^-) \times g + L$$

where, at processor i, w_i is the local sequential work performed during the computation phase, h_i^+ is the size of data sent from i to other processors, and h_i^- the size of the received data by processor i from other processors.

b) BSML parallel vectors: BSML designed as a full language, is currently implemented as a library for the Objective Caml language [8]. BSML offers an access to the parameters of the underlying BSP architecture with the constants: bsp_p (an integer), bsp_g, bsp_l, and bsp_r (floats).

A BSML program is not written in the Single Program Multiple Data (SPMD) style as many parallel programs are, but offers a *global view* of the parallel program. It is a usual OCaml program plus operations on a parallel data structure. This structure is called parallel vector and it has an abstract polymorphic type 'a par. A parallel vector has a fixed width bsp_p equals to the constant number of processes in a parallel execution. We will informally write $\langle x_0, \ldots, x_{n-1} \rangle$ for a parallel vector of size n, which contains the value x_i at processor i. The nesting of parallel vectors is forbidden. BSML provides four primitives for the manipulation of parallel vectors. For each of these primitives, we will give its signature, its informal semantics, examples of use and its formalisation in Coq.

In the Coq developments of our framework, all the modules related to parallelism are functors that take as argument a module which provides a realization of the semantics of BSML. This semantics is modelled in a module type called BSML_SPECIFICATION. A module type or module signature in Coq is a set of definitions, parameters and axioms, the latter being types without an associated proof terms.

The module type BSML_SPECIFICATION contains : the definition processor of processor names, and associated axioms; the type par of parallel vectors; the axioms which define the semantics of the four parallel primitives of BSML.

A natural processor_max is assumed to be defined. The total number of processor, the BSP parameter bsp_p is the successor of this natural. The type processor is defined as:

Definition processor := { pid: nat | pid < bsp_p }.

A term of type {x:A | P x} is a value of type A and a proof that this value verify the property P. A value of type processor thus a pair: A natural and a proof that this natural is lower than bsp_p.

The type of parallel vectors is an opaque type

Parameter par: **Set**→**Set**.

This means that par could take as argument a type and returns a new type: It is a polymorphic type thus it has as argument the type of the values contained in the vectors. For example, the type for parallel vectors of string could be written par string (and is written string par in BSML).

In the informal semantics above, a parallel vector consists of p values. As the type par is opaque in the formalisation in Coq, to model this we assume that the type par comes with a function get that can access any component of the parallel vector:

Parameter get : ∀A: **Set**, par A →processor →A.

Given a type T, a parallel vector vec of type par T and a processor i, (get T vec i) is the value held by processor i in parallel vector vec.

c) Parallel vector creation: The primitive mkpar is used to create parallel vectors. It takes a function f as argument and creates a parallel vector that contains the value of $(f\ i)$ at processor i, $0 \leq i <$ bsp_p. The signature of this primitive is: mkpar: (int→ 'a) → 'a par, and informally its semantics could be written as:

$$\text{mkpar}\ f = \langle f\ 0, \ldots, f\ (\text{bsp\_p} - 1) \rangle$$

In the interactive loop of BSML [8][1] on a machine with 6 processors, one could have the following result:

```
# let this = mkpar (fun i → i);;
val this : int Bsml.par = <0, 1, 2, 3, 4, 5>
```

is the prompt inviting us to write an expression to evaluate. The expression **let** this =... is used to define the value this by applying the primitive mkpar to the (anonymous) identity function. In the second line, the top-level answers that a value (**val**) called this has been defined, that it is a parallel vector of integers (int Bsml.par) and its value is $\langle 0, 1, 2, 3, 4, 5 \rangle$.

A useful function is replicate:

```
# let replicate = fun x → mkpar(fun _ → x);;
val replicate : 'a → 'a Bsml.par = <fun>

# let vx = replicate "PDCAT";;
vx : string Bsml.par = <"PDCAT", "PDCAT", "PDCAT",
                        "PDCAT", "PDCAT", "PDCAT">
```

The evaluation of an application of mkpar to a function f is done without any communication (any usual Objective Caml value is replicated on all the processors, hence the function f), and is done during the asynchronous computation phase of a BSP super-step. Its BSP cost is $\max_{0 \leq i < \text{bsp\_p}} \overline{f\ i}$ where $\overline{e}$ denotes the time required to evaluate expression e.

The semantics of the parallel primitives of BSML in Coq are specified using the get function. It is a quite straightforward translation of the informal semantics. Instead of giving the result parallel vector as a whole it is described by giving the values of its components. A Coq formalisation of the mkpar primitive is thus:

Parameter mkpar_specification : ∀(A:**Set**) (f: processor →A),
{ X: par A | ∀i: processor, get X i = f i }.

[1]In case the reader wants to try the examples in the interactive loop (command `bsml`) while reading the paper, she needs to write **open** Bsml;; at the beginning of the session.

d) Point-wise parallel application: As the type par and the primitive mkpar are polymorphic, it is possible to create parallel vectors of functions. For example:

```
# let vf = mkpar(fun i→ fun x → x ^ (string_of_int i));;
val vf : (string → string) Bsml.par = ≪ fun>, ..., <fun≫
```

Thus we may need to apply a parallel vector of functions to a parallel vector of values. Such an application is not a usual functional application: We need a primitive to perform it. This primitive is called apply. Its signature is: apply:('a→ 'b)par→ 'a par→ 'b par.

For example, we can apply the parallel vector of functions vf to the parallel vector of values vx:

```
# let v2 = apply vf vx;;
val v2 : string Bsml.par = <"PDCAT0", "PDCAT1", "PDCAT2",
                            "PDCAT3", "PDCAT4", "PDCAT5">
```

The informal semantics of apply could be written as:

$$\text{apply } \langle f_0, ..., f_{p-1} \rangle \ \langle x_0, ..., x_{p-1} \rangle = \langle f_0 \ x_0, ..., f_{p-1} \ x_{p-1} \rangle$$

The evaluation of an application of apply requires no communication. Its BSP cost is $\max_{0 \leq i < \text{bsp\_p}} \overline{f_i \ x_i}$. In Coq, apply is specified as follows:

```
Parameter apply_specification :
  ∀(A B: Set) (vf: par (A →B)) (vx: par A),
  { X: par B | ∀i: processor, get X i = (get vf i) (get vx i) }.
```

Very useful functions which are part of the BSML standard library could be implemented from mkpar and apply such as:

```
(* parfun: ('a → 'b) → 'a par → 'b par *)
let parfun = fun f v → apply (replicate f) v
```

There exist variants parfunN of parfun for point-wisely applying a sequential function with N arguments to N parallel vectors.

e) Communications: The two last primitives of BSML require both communication and synchronisation.

The primitive put is used to exchange data between processes. It takes and returns a parallel vector of functions. At each processor the input function describes the messages to be sent to other processors and the output function describes the messages received from other processors. Its signature is put:(int→ 'a)par→ (int→ 'a)par and it informal semantics follows:

$$\text{put } \langle f_0, \ldots, f_{p-1} \rangle = \langle \lambda i.f_i 0, \ldots, \lambda i.f_i(p-1) \rangle$$

At processor i, the function f_i encodes the bsp_p possible messages to be sent to other processors. Some values, as the empty list or the first constructor without parameters in a sum type, are considered to mean "no message". For example if we want to broadcast a value at a specific processor in a parallel vector to all other processors, we will use two functions: One that will send this value to every processor, one that will send nothing to every processor. The first of these functions should be used by the processor that is the root of the broadcast, and the second function by all other processors:

```
let choice root pid value =
  let toall = fun dst → [value]
  and nothing = fun dst → [] in
  if pid=root then toall else nothing
```

Then after the function choice is used to build a parallel vector of functions describing the communications we want to perform, and this parallel vector is given as argument to put, we need to retrieve the sent values. Here again the bsp_p possible received values by a processor are encoded as a function. To know the value sent by a processor i to a processor j, one has to apply, at processor j, the obtained function to i. In the case of the broadcast, all processors need to apply the function to the processor root:

```
# let broadcast root vx =
    let msg = apply (mkpar(choice root)) vx in
    parfun List.hd (apply (put msg) (replicate root));;
val broadcast : int → 'a Bsml.par → 'a Bsml.par = <fun>

# let v3 = broadcast 3 v2;;
val v3 : string Bsml.par = <"PDCAT3", "PDCAT3", "PDCAT3",
                            "PDCAT3", "PDCAT3", "PDCAT3">
```

The evaluation of an application of the put primitive requires a full super-step: First the messages are computed from the parallel vector of functions describing the messages to send; then the messages are exchanged; and a global synchronisation ends the super-step in order to allow the functions describing the received messages to be built. In the following, we note $|\, v \,|$ the size (in words) of the value v, and $(e)\!\downarrow$ the result of the evaluation of the expression e. The BSP cost of put is:

$$\max_{0 \leq i < \text{bsp\_p}} \Big(\sum_{j=0}^{\text{bsp\_p}-1} \overline{f_i \ j} \Big) \ + \ \max_{0 \leq i < \text{bsp\_p}} (h_i^+, h_i^-) \times g \ + \ L$$

$$\text{where } \begin{cases} h_i^+ & = & \sum_{0 \leq j < \text{bsp\_p}}^{j \neq i} |\, (f_i \ j)\!\downarrow \,| \\ h_i^- & = & \sum_{0 \leq j < \text{bsp\_p}}^{j \neq i} |\, (f_j \ i)\!\downarrow \,| \end{cases}$$

h_i^+ is the total size of the messages sent by processor i, while h_i^- is the total size of the messages received by processor i.

The Coq specification of put is:

```
Parameter put_specification :
  ∀(A:Set) (vf: par (processor →A)),
  { X: par (processor →A) | ∀i j: processor, get X i j = get vf j i }.
```

The last primitive proj:'a par→ (int→ 'a) is the inverse of mkpar (for functions defined on the domain of processor names). It also requires a full super-step, we omit the details for the sake of conciseness.

B. BH in BSML

As a larger BSML example, we present in Fig. 4 a parallel implementation of the BH algorithmic skeleton where sequence n1 n2 returns the list [n1 ; ... ; n2], bh_seq is a sequential implementation of BH and type of communicated data is **type** ('l,'r) comm_type = Lcx **of** 'l | Rcx **of** 'r | Ncx.

Given a list that is split into local chunks in the order of the processors, the preliminary local computation (applying gl and gr) can be done with the apply primitive; then a put is used to

```
let bh_par k gl opl gr opr = fun (l: 'l) (lst: 'a list par) (r: 'r) →
  let accl = parfun gl lst
  and accr = parfun gr lst in
  let comms = put(apply(
                        apply(
                            mkpar(fun i accr accl receiver →
                                if i < receiver then Lcx accl
                                else if i > receiver then Rcx accr
                                else Ncx)) accl)accr) in
  let lv = parfun2
          (fun c pl →
             fold_left
                 (fun acc i → opl acc (match c i with Lcx x→ x))
                 l
                 pl)
           comms
           (mkpar(fun pid→ sequence 0 (pid−1)))
  and rv = parfun2
          (fun c pr →
             fold_right
                 (fun i acc → opr (match c i with Rcx x→ x) acc)
                 pr
                 r)
            comms
            (mkpar(fun pid→ sequence (pid+1) (bsp_p−1))) in
    parfun3 (bh_seq k gl opl gr opr) lv lst rv
```

Fig. 4
AN IMPLEMENTATION OF BH IN BSML

send the computed r value leftwards to every other processor, and the l value rightwards. apply is then sufficient to locally compute BH on the local chunks using the sequential version of BH.

However this implementation is not proved correct to the definition of BH.

C. Proving Correct BSML Implementation of BH in Coq

From the axioms presented in subsection V-A above, we obtain four Coq functions that verify the BSML specifications. These functions and their properties are used when we prove the correctness of a parallel version of BH with respect to definition 1.

The main theorem is:

```
Theorem bh_bsml_bh lst: ∀(L A R B:Set) (k:L→A→R→B)
  (gl: list A→L) (opl:L→L→L) (gr:list A→R) (opr:R→R→R)
  (gl_hom : is_homomorphism A L gl opl)
  (gr_hom : is_homomorphism A R gr opr)
  (lst:list A),
  list_of_par_list(bh_bsml_comp (gl nil) (scatter lst) (gr nil))) =
  bh_comp k gl opl gr opr (gl nil) lst (gr nil).
```

It states the equivalence of bh_comp and the parallel version bh_bsml_comp. The bh_bsml_comp function is quite similar to the direct implementation of BH in BSML given in Fig. 4. This function takes a distributed list (or parallel vector of lists) as input (type 'a list par in BSML and par(list A) in Coq), and also returns a distributed list whereas bh_comp takes as input a list and returns a list. Thus some conversions are needed. scatter takes a list and cuts it into pieces that are distributed. list_of_par_list does the inverse: it takes a parallel vector of lists converts it to a function from natural to lists (using a variant of the proj primitive) and eventually merges the lists into one list.

In order to prove this theorem, two intermediate results are necessary. They state that at a given processor, the Coq formalisation of vl and vr in Fig. 4 are respectively equal to applying gl (resp. gr) to the sub-list of elements being on the left (resp. the right) of the local sub-list. The proofs are technical and use several steps where sub-lists are cut and combined. The proofs (available in [27]) are done by considering the processor list as being the list (l1++p::nil)++l2 and by reasoning by induction on l1 for vl and on l2 for vr.

D. BSP Cost of Parallel BH

The BSML implementation of *BH* applied to parameters k, gl, $\oplus_l$, gr, $\otimes_r$ and lst (assumed to be values) has the following complexity: The computation is done in one full super-step followed by some asynchronous local computations. We assume each processor i has a contiguous part of the list lst, that will be denoted by lst_i.

The BSP cost of the application of BH is:

$$seq_1 + \max_{0 \leq i < \text{bsp\_p}} \max(h_i^+, h_i^-) \times g + L + seq_2$$

The first phase computes on each processor, the two "summaries" of the values held by the processor: One to be sent to processors at its left (computed with gl), and one to be sent to the processors at its right (computed with gr):

$$seq_1 = \max_{0 \leq i < \text{bsp\_p}} (\overline{gl\ lst_i} + \overline{gr\ lst_i})$$

After that, each processor i sends $l_i = (gl\ lst_i)\downarrow$ to processors with smaller processor names and $r_i = (gl\ lst_i)\downarrow$ to processors with greater processor names. So the size of exchanged data is:

$$\begin{cases} h_i^+ &= i \times |\, l_i \,| + (\text{bsp\_p} - 1 - i) \times |\, r_i \,| \\ h_i^- &= \sum_{j=0}^{i-1} |\, r_j \,| + \sum_{j=i+1}^{\text{bsp\_p}-1} |\, l_j \,| \end{cases}$$

The remaining asynchronous local computation proceeds as follows: First, on each processor the received list of left (resp. right) summaries is reduced with $\oplus_l$ (resp. $\otimes_r$). Then a local sequential BH is performed: It is implemented as the computation of the local left and right summaries for each element of the local list yielding to lists ll_i and lr_i. However each of these lists can be computed with traversing only once the local list lst_i. The computation ends with apply k to each triple of the list obtained as the combination of the three lists ll_i, lst_i, and lr_i. We do not detail the cost in the general case. In cases where $\oplus_l$, $\otimes_r$, and k have constant complexity, and gr and gl have constant complexity on singleton lists, we have: $seq_2 \in \mathcal{O}(length\ lst_i)$.

Tower building complexity: With the example of tower building, $\oplus_l = \otimes_r = \uparrow$ has constant complexity providing that we can do a comparison in constant time. $maxAngleL$ and $maxAngleR$ have the same complexity:

$$\overline{maxAngleL\ lst} = \overline{maxAngleR\ lst} \in \mathcal{O}(length\ lst).$$

So extracted Tower Building has complexity:

$$\max_{0 \leq i < \text{bsp\_p}} \mathcal{O}(length\ lst_i) + g * \mathcal{O}(p) + L$$

Thus, if the list lst is evenly distributed the complexity is:

$$\mathcal{O}(length\ lst/p) + g * \mathcal{O}(p) + L.$$

VI. Programs Extraction and Experiments

Let us summarise the different steps towards a proved correct parallel implementation of the tower building problem: (a) First we specify the problem as an instance of mapAround; (b) using theorem 1, we prove that the problem is an instance of BH; (c) using theorem bh_bsml_bh, we prove that the problem is an instance of the parallel version of BH.

From this latter proof, we can extract an implementation of the tower building problem in BSML. The resulting code is of course similar in structure of the code of the direct implementation of BH in BSML (Fig. 4). The main differences are on the sequential data structures. The lists type are the one defined inductively in Coq, not the optimised ones defined in Ocaml. The BSML primitives in Coq manipulate processor and nat which rely on a Peano encoding of naturals. Thus the extracted code contains: **type** nat = | O | S **of** nat which is very inefficient for computations.

To test the differences of efficiency between extracted and non-extracted programs, we experimented with two different tower building programs: The direct implementation and the implementation extracted from the derivation in Coq. The experiments were conducted on the "Centre de Calcul Scientifique de la Région Centre (CCSC)" which is a 42 nodes cluster of IBM blades with 2 quad-core Xeon E5450 and 8 Gb of memory per blade.

We had to use only one core per CPU due to a dramatic loss of performance in MPI communications when multiple cores of the same CPU try to access to the network, and we were able to book up to 18 nodes of the cluster.

In order to avoid the garbage collector of OCaml to be triggered too often, we grew the minor heap size to 1 Gb. We performed a garbage collection after the computation and took its time into account in our benchmark. Indeed, when the data are to big to fit in the minor heap, the recurrent calls to the garbage collector dramatically hinder the overall performance.

Figure 5 shows, for 12 processors the computation time for different sizes of data. We can see that the programs execution time (and the garbage collection time) grows linearly with the amount of data. The extracted version of the program is slower than the direct implementation with a time factor between 1 and 2.5. As said earlier, this come most probably from the difference in data structure encoding.

As shown by figure6, the speedup of both implementation is linear with the number of processors, for a fixed amount of data (5.120.000 elements).

We also performed experiments on the Maximum Prefix Sum example on another cluster, up to 32 processors. Figure 7 also shows a linear speedup (320.000 elements).

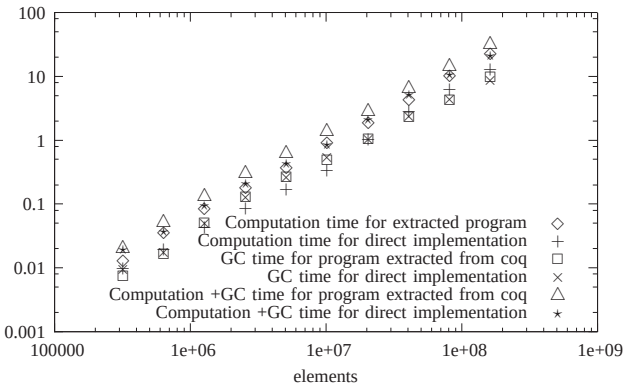

Fig. 5
Tower Building Timings (in s)

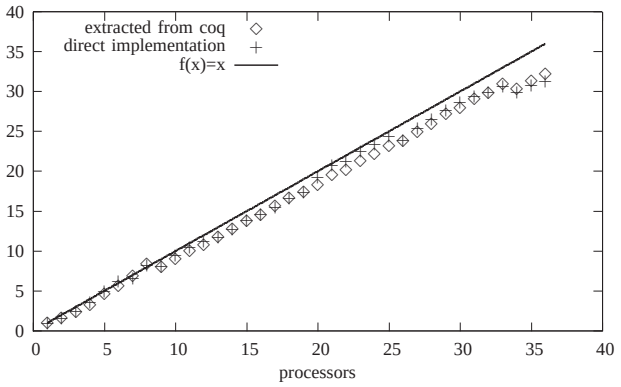

Fig. 6
Tower Building Speedup

VII. Related Work

Our framework combines two strength: constructive algorithmic and proved correct bulk synchronous parallel language. In this paper we focus on the semantics of the programming model of Bulk Synchronous Parallel ML, which was first express as an extension of the λ-calculus [19]. It is possible to implement this semantics as a sequential program, for example by realizing parallel vectors by lists or arrays. But as it is traditional in data-parallel languages, we also provide the semantics of an execution model which describes the parallel implementation of BSML programs as SPMD programs. We even propose a semantics which is even closer to the real implementation: a parallel abstract machine. All these semantics have been proved equivalent [9]. Thus proving the correctness of a BSML program using the semantics of the

Fig. 7
Maximum Prefix Sum Speedup

programming model of BSML ensures *the parallel execution* of this program will also be correct (up to the correctness of the implementation of the parallel abstract machine).

But BSML relies, for communications, on the put operation which is not very easy to use. On the contrary, constructive algorithmic provides various ways to help the programmer of parallel applications to systematically derive efficient parallel algorithms [7], [12]. However there is a gap between the final composition of algorithmic skeletons obtain by derivation and its implementation. Usually in skeletal parallelism, the semantics of the execution model remains informal. Exception are [1], [5]. Several researchers worked on formal semantics for BSP computations, for example [17], [23], [25]. But to our knowledge none of these semantics was used to generate programs as the last step of a systematic development.

On the contrary LOGS [6] is a semantics of BSP programs and was used to generate C programs [30]. Compared to our approach, there is a gap between the primitives of the semantics and the implementation in C. Our programs are extracted from Coq proof terms. There exists work on bulk synchronous parallel algorithmic skeletons [13], [29]: in these approaches the derivation or optimisation process is guided by the BSP cost model, but the gap between the final composition of skeletons and the implementation is still there.

It is worth noting that the idea of using formal approaches for transforming abstract specifications into concrete implementations was also proposed as abstract parallel machines in [21]. Compared with the study, the contribution of the paper is that we proposed and realized a concrete framework based on the Coq proof assistant and the BSP model.

VIII. Conclusion

In this paper, we report our first attempt of combining the theory of constructive algorithmic and proved correct BSML parallel programs for systematic development of certified BSP parallel programs, and demonstrate how it can be useful to develop certified BSP parallel programs. Our newly proposed framework for the certified development of programs includes the new theory for the BH homomorphism, and an integration of Coq (for specification and development interaction), the BH homomorphism and BSML programming. All the certification of the transition from specification to algorithms in BH and to certified BSML parallel programs is done with the Coq proof assistant. We prove, in Coq, theorems validating the transformations of a simple, sequential specification into a more detailed and complex parallel specification. Then, using the program-extraction features of Coq yields a certified parallel program.

References

[1] M. Aldinucci and M. Danelutto. Skeleton-based parallel programming: Functional and parallel semantics in a single shot. *Computer Languages, Systems and Structures*, 33(3-4):179–192, 2007.

[2] Y. Bertot. Coq in a hurry, 2006. http://hal.inria.fr/inria-00001173.

[3] Y. Bertot and P. Castéran. *Interactive Theorem Proving and Program Development*. Springer, 2004.

[4] G. E. Blelloch. Scans as primitive operations. *IEEE Trans. on Computers*, 38(11):1526–1538, 1989.

[5] A. Cavarra, E. Riccobene, and A. Zavanella. A formal model for the parallel semantics of p3l. In *SAC '00: Proceedings of the 2000 ACM symposium on Applied computing*, pages 804–812. ACM, 2000.

[6] Y. Chen and J. W. Sanders. Logic of global synchrony. *ACM Transaction on Programming Languages and Systems*, 26(2):221–262, 2004.

[7] M. Cole. Parallel programming with list homomorphisms. *Parallel Processing Letters*, 5(2):191–203, 1995.

[8] F. Loulergue et al. The BSML Library version 0.4. `https://traclifo.univ-orleans.fr/BSML`, 2008.

[9] F. Gava. *Approches fonctionnelles de la programmation parallèle et des méta-ordinateurs. Sémantiques, implantations et certification.* PhD thesis, University Paris Val-de-Marne, LACL, 2005.

[10] A. V. Gerbessiotis and L. G. Valiant. Direct bulk-synchronous parallel algorithms. *Journal of Parallel and Distributed Computing*, 22(2):251–267, 1994.

[11] S. Gorlatch. Systematic efficient parallelization of scan and other list homomorphisms. In *Euro-Par'96. Parallel Processing*, LNCS 1124, pages 401–408. Springer-Verlag, 1996.

[12] S. Gorlatch. Systematic extraction and implementation of divide-and-conquer parallelism. In *Proc. Conference on Programming Languages: Implementation, Logics and Programs*, LNCS 1140, pages 274–288. Springer-Verlag, 1996.

[13] Y. Hayashi and M. Cole. Automated cost analysis of a parallel maximum segment sum program derivation. *Parallel Processing Letters*, 12(1):95–111, 2002.

[14] Z. Hu, H. Iwasaki, and M. Takeichi. Formal derivation of efficient parallel programs by construction of list homomorphisms. *ACM Transactions on Programming Languages and Systems*, 19(3):444–461, 1997.

[15] Z. Hu, H. Iwasaki, and M. Takeichi. An accumulative parallel skeleton for all. In *11st European Symposium on Programming (ESOP 2002)*, LNCS 2305, pages 83–97. Springer, 2002.

[16] Z. Hu, M. Takeichi, and W.-N. Chin. Parallelization in calculational forms. In *25th ACM Symposium on Principles of Programming Languages (POPL'98)*, pages 316–328. ACM Press, 1998.

[17] H. Jifeng, Q. Miller, and L. Chen. Algebraic laws for BSP programming. In *Euro-Par'96. Parallel Processing*, LNCS 1123–1124. Springer, 1996.

[18] R. Loogen, Y. Ortega-Mallén, R. Peña, S. Priebe, and F. Rubio. Parallelism Abstractions in Eden. In *Patterns and Skeletons for Parallel and Distributed Computing*, LNCS 2011. Springer, 2002.

[19] F. Loulergue, G. Hains, and C. Foisy. A Calculus of Functional BSP Programs. *Science of Computer Programming*, 37(1-3):253–277, 2000.

[20] K. Morita, A. Morihata, K. Matsuzaki, Z. Hu, and M. Takeichi. Automatic inversion generates divide-and-conquer parallel programs. In *ACM SIGPLAN 2007 Conference on Programming Language Design and Implementation (PLDI 2007)*, pages 146–155. ACM Press, 2007.

[21] J. O'Donnell and G. Rünger. Abstract parallel machines. *Computers and Artificial Intelligence*, 19(2), 2000.

[22] D. Skillicorn. *Foundations of Parallel Programming*. Cambridge University Press, 1994.

[23] D. Skillicorn. Building BSP Progams Using the Refinement Calculus. In *Workshop on Formal Methods for Parallel Programming: Theory and Applications*, LNCS, pages 790–795. Springer, 1998.

[24] M. Sozeau. *Coq 8.2 Reference Manual*, chapter Type Classes. INRIA TypiCal, 2008.

[25] A. Stewart, M. Clint, and J. Gabarró. Barrier synchronisation: Axiomatisation and relaxation. *Formal Aspects of Computing*, 16(1):36–50, 2004.

[26] The Coq Development Team. The Coq Proof Assistant. `http://coq.inria.fr`.

[27] The SDPP Development Team. Systematic Development of Parallel Programs. `https://traclifo.univ-orleans.fr/SDPP`.

[28] L. G. Valiant. A bridging model for parallel computation. *CACM*, 33(8):103, 1990.

[29] A. Zavanella. Skeletons, BSP and performance portability. *Parallel Processing Letters*, 11(4):393–405, 2001.

[30] J. Zhou and Y. Chen. Generating C code from LOGS specifications. In *2nd International Colloquium on Theoretical Aspects of Computing (ICTAC'05)*, LNCS 3407, pages 195–210. Springer, 2005.

Appendix

A Very Short Introduction to Coq

The Coq proof assistant [26] is based on the calculus on inductive construction. This calculus is a higher-order typed

λ-calculus. Theorems are types and their proofs are terms of the calculus. The Coq systems helps the user to build the proof terms and offers a language of tactics to do so.

We illustrate quickly all these notions on a short example :

```
Inductive nat:Set := O : nat | S : nat →nat.

Fixpoint plus (n1 n2 : nat) {struct n1} : nat :=
match n1 with
  | O ⇒n2
  | S n ⇒S(plus n n2)
end.

Lemma plus_n_O : ∀n, plus n O = n.
  induction n.
  (* case n=0 *) simpl. reflexivity.
  (* case n>0 *) simpl. rewrite IHn. reflexivity.
Qed.

Definition pred : ∀n:nat, n<>O→{q:nat|(S q)=n}.
  intros.
  destruct n.
    (* case n=0 *) elim H. reflexivity.
    (* case n>0 *) exists n. reflexivity.
Defined.
```

In this example, we first define a new inductive type, the type of natural numbers in the Peano style. nat has type **Set** which means it belongs the computational realm of the Coq language. We also define the plus recursive function on naturals. In this recursive definition we specify the decreasing argument (here n1) as all functions must be terminating in Coq. In both cases, we gave the type of the new name we wanted to define as well as a term of this type.

We then define a lemma named plus_n_O which states that ∀n, plus n O = n. If we check (using the Check command of Coq) the type of expression, we would obtain Prop which mean that this expression belongs to the logical realm. To define plus_n_O we also should provide a term of this type, that is a proof of this lemma. We could write directly such a term, but it is usually complicated and Coq provides a language of tactics to help the user to build a proof term. If we give to Coq top-level the line beginning with **Lemma** we would enter the interactive proof mode that would indicate us that we should prove the goal:

```
  ============================
   forall n : nat, plus n O = n
```

We prove this goal by induction on n using the tactic induction n. The system indicates now two goals to prove:

```
  ============================
   plus O O = O

subgoal 2 is:
 plus (S n) O = S n
```

The first one is proved using the definition of plus using the tactic simpl which yields the goal 0 = 0 and this case is ended by the application of the tactic reflexivity. The second one is the inductive case:

```
  n : nat
  IHn : plus n O = n
  ============================
   plus (S n) O = S n
```

After simplification, we obtain the goal S(plus n O) = S n. We solve it first by rewriting plus n O in n using the IHn hypothesis and then we conclude by reflexivity.

Mixing logical and computational parts is possible in Coq. For example a function of type A→B with a precondition P and a postcondition Q corresponds to a constructive proof of type: ∀x:A, (P x) →exists y:B →(Q x y). This could be express in Coq using the inductive type sig:

```
Inductive sig (A:Set) (Q:A→Prop) : Set := | exist: ∀
(x:A), (Q x) →(sig A Q).
```

It could also be written, using syntactic sugar, as {x:A|(P x)}.

This feature is used in definition of the function pred. The specification of this function is: ∀n:nat, n<>O→{q:nat|(S q)=n} and we build it using tactics. We reason by case on n (tactic destruct). The first case is easily solved because we have the hypothesis O<>O, the second one is trivial.

The command **Extraction** pred would extract the computational part of the definition of pred. We could obtain a certified implementation of the predecessor function:

```
(** val pred : nat →nat **)
let pred = function
  | O →assert false (* absurd case *)
  | S n0 →n0
```

[2] is a quick yet longer introduction to Coq.